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**METHOD OF REDUCING A RIPPLE OF A HEAVY LOADING PULSE
FREQUENCY MODULATED VOLTAGE REGULATOR**

Appl. No. : 10/707,166 Confirmation No.: 1165
Applicant(s) : Guang-Nan TZENG,
Yung-Chih CHEN,
Rong-Chin LEE
Filing Date : November 25, 2003
TC/A.U. : 2838
Examiner : Matthew Van NGUYEN
Docket No. : AITP0003USA
Customer No. : 27765

Commissioner for Patents
P. O. Box 1450
Alexandria VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

This paper is responsive to the Final Office Action mailed on January 4, 2006, having a shortened statutory period for response set to expire April 4, 2006. In light of the Amendments and/or Remarks herein, further examination is requested.

Amendments to the claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.